

Fig. 1

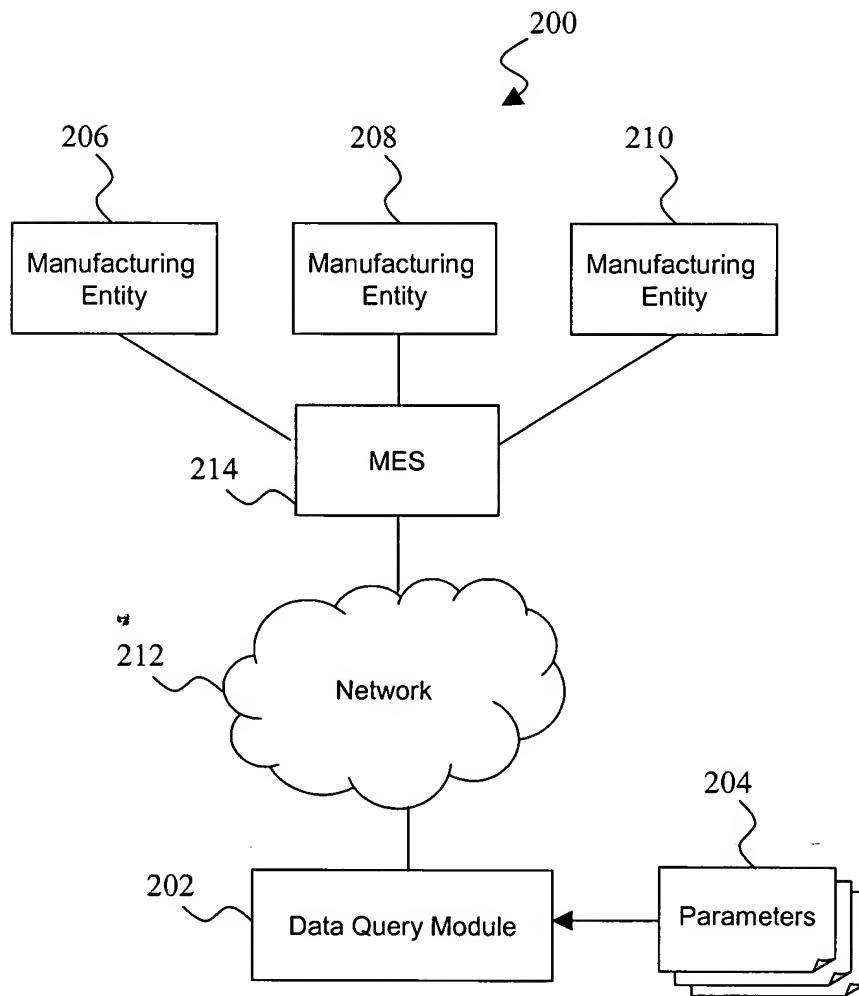


Fig. 2

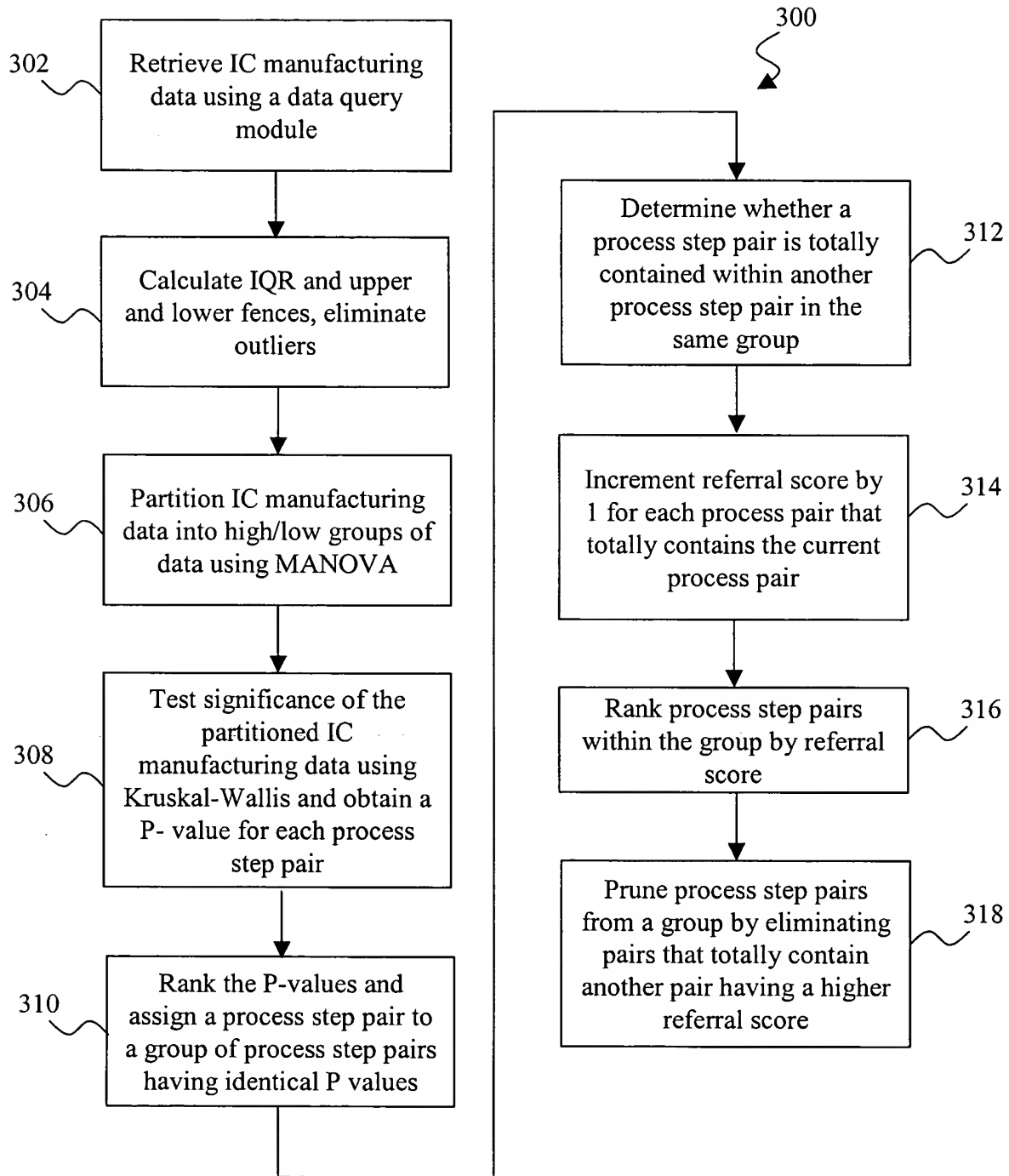
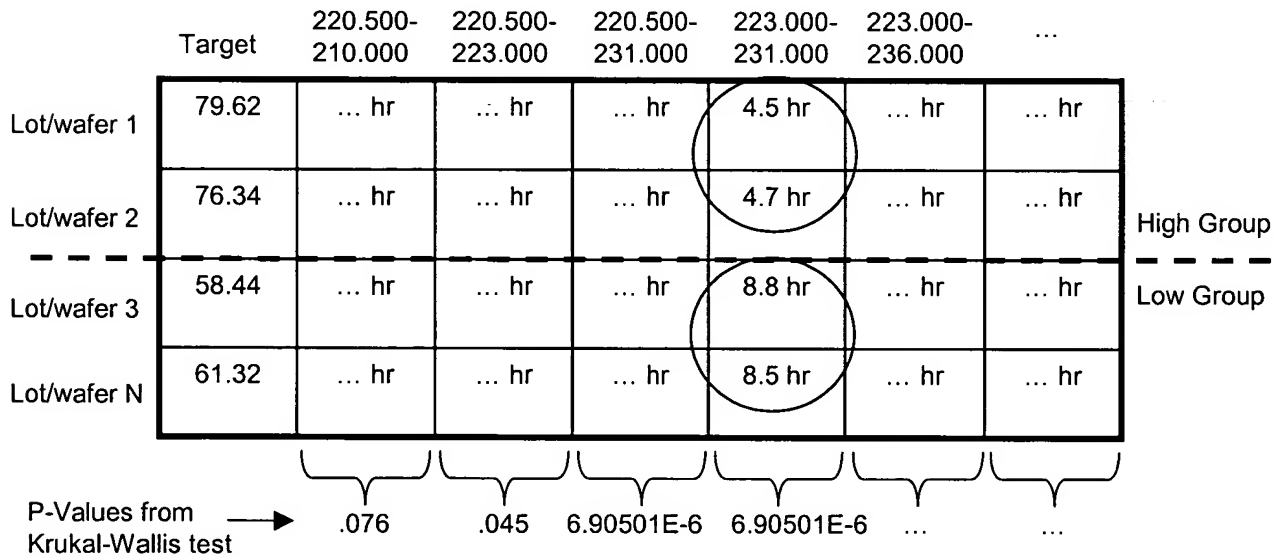
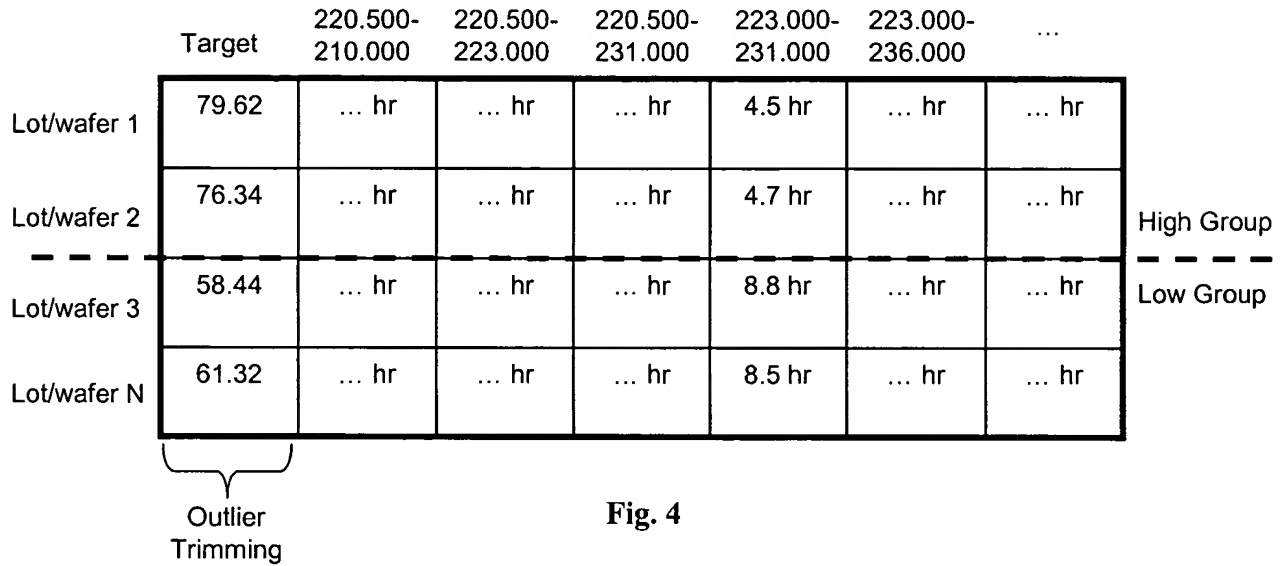


Fig. 3

Attorney Docket No. 24061.77
A SYSTEM AND METHOD FOR IDENTIFYING SEMICONDUCTOR PROCESS STEPS
FOR QUEUE-TIME CONTROL AND ABNORMALITY DETECTION

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Attorney Docket No. 24061.77
*A SYSTEM AND METHOD FOR IDENTIFYING SEMICONDUCTOR PROCESS STEPS
FOR QUEUE-TIME CONTROL AND ABNORMALITY DETECTION*

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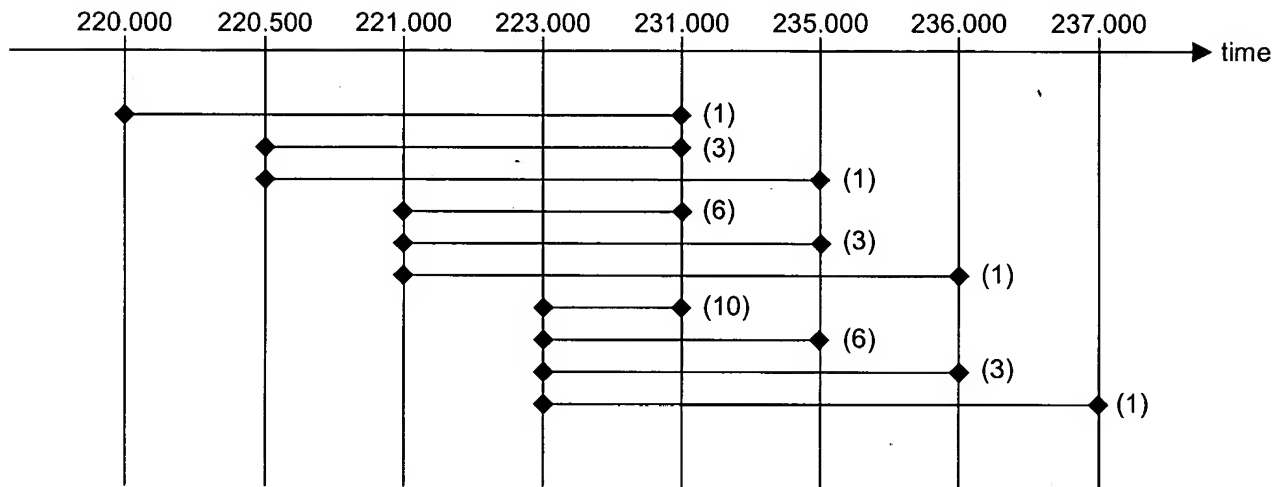


Fig. 6